Memory Addressing the Apple Graphics Screen

E.S. deSa*, and E.J. D'Sa*

1. Introduction

When the Apple II is set in the High Resolution graphics mode, it can display 53,750 dots in a matrix defined by 280 dots wide and 192 dots high. As is commonly known, the primary page of the picture buffer (invoked by the Applesoft HGR command) is a 8192 byte area of memory covering the address range 8192 to 16393. Similarly, the secondary page (invoked by the HGR2 command) extends over the address range 16394 to 24575.

Frequently, in writing graphics software on the Apple, it is useful to know relationship between a dot's (x,y) coordinates to its memory address in the graphics area. A glance at the memory map of the high-res screen of the Apple shows that the organisation of addresses do not follow a well-ordered sequential scheme over the full memory page. However, by dividing the screen matrix into three separate memory blocks, the memory address of any dot belonging to a byte can be related to its (x,y) coordinates on the Apple screen matrix by the following relations.

For the HGR page:

M1 =
$$8192 + Qx + Qy * 128 + 1024 * 8 * Ry$$
 (1) valid over the coordinate range $(0,0) \le (x,y) \le (279,63)$ M2 = $8232 + Qx + (Qy - 8) * 128 + 1024 * 8 * Ry$ (2) Valid over the coordinate range $(0,64) \le (x,y) \le (279,127)$ M3 = $8272 6B Qx + (Qy - 16) * 128 + 1024 * 8 * Ry$ (3) Valid over the coordinate range $(0.128) \le (x,y) \le (129,159)$ and excluding the text window.

The addresses M1, M2 & M3 correspond to the three memory blocks comprising the HGR page, and

Qx = Quotient of (x/7)

Qy = Quotient of (y/8)

Ry = Remainder of (y/8)

Instrumentation and Computer Division, National Institute of Oceanography, Dona Paula, Goa 403 004, In like manner, the HGR2 page is defined by the equations:

$$N1 = 16384 + Qx + (Qy) * 128 + Ry * 1024 * 8$$
 (4)
 $N2 = 16424 + Qx + (Qy - 8) * 128 + Ry * 1024 * 8$ (5)
 $N3 = 16464 + Qx + (Qy - 16) * 128 + Ry * 1024 * 8$ (6)

The quantities Qx, Qy & Ry and coordinate ranges are as given for the HGR page, except that for block N3 (equation 6) the coordinates (x,y) range from (0,128) to (279,192).

Figure 1 shows the high-resolution graphics area partitioned into three separate memory blocks. There are eight box rows in each block, and each box row has eight "line rows". A line row is 40 bytes wide with sequential addresses running along any line row.

Further examination of the screen matrix shows that for a given block and a fixed column:

- * Corresponding line rows between neighbouring box rows are separated by 128 bytes.
- * Neighbouring line rows of a given box row are separated by 1024 bytes.

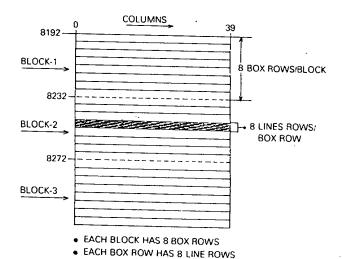


Fig.1 HGR map of the Apple high-resolution Screen

With these facts in mind, it is easy to deduce the equations listed above. The variable Qx signifies the column number in the range 0 to 39; Qy + 128 selects the box row number; Ry* 1024 * 8 selects the line row within the selected box row. In this way the address of any block data byte in graphics memory can be computed.

A simple nest of FOR - NEXT LOOPS will enable a user to examine or modify address contents in any selected memory block, e.g.

10 FOR I = 0 TO 7

: Box row counter

20 FOR J = 0 to 7

: Line row counter

30 FOR K = 0 to 39

: Column counter

40 MI = 8192 + 128 * 1 + 1024 * J + K

50 POKE (MI), 255

 $60 \times = PEEK (M1)$ 70 NEXT K: NEXT J: NEXT I

2. Use of Memory Equations in Horizontal Scrolling

As a further example of the use of the above equations, an Applesoft Basic program was written to implement horizontal scrolling of a single wave form. The program list out is shown in Appendix I.

The program plots a sine wave in the memory block I of the Apple HGR page for coordinates in the range (0,0) to (279,63). Whilst plotting the (x,y) points of the sine wave, the address GADD of a data point, column number Qx and data byte LU are determined and stored in the matrix arrays M, C and D.

By pressing the right cursor key \rightarrow the right scroll routine is executed at line 700. In order to do right scrolling, the waveform needs to be translated into a fixed number of bytes, D. Each successive press of the cursor key adds an integral number of D bytes to the addresses of data points in the original waveform over successively smaller plotting sections of the curve. In our case, each key press removes eight data addresses from the total number of data addresses.

The left scroll routine executes at line 500 after a key press of the left cursor key ←. It is similar to right scrolling except that the shift of amount D bytes is subtracted from the addresses of original data points.

3. Summary

The graphics address equations derived here provide a useful tool when writing graphics software. An example of its use in horizontal scrolling was implemented without having to write a lengthy machine language routine. As a simple extension, a BASIC program could be written to implement vertical scrolling of a graphics picture using these equations.

APPENDIX I: A Sample Program